

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kie Y. Ahn et al.

Examiner:

David S. Blum

Serial No.:

09/945535

Group Art Unit:

2813

Filed:

d

August 30, 2001

Docket:

1303.026US1

Title:

HIGHLY RELIABLE AMORPHOUS HIGH-K GATE OXIDE ZrO2

INFORMATION DISCLOSURE STATEMENT

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

Filing Date: August 30, 2001

Title: HIGHLY RELIABLE AMORPHOUS HIGH-K GATE OXIDE ZrO2

Page 2 Dkt: 1303.026US1

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

KIE Y. AHN ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6944

Date 2 - 9 - 04

David C. Peterson

Reg. No. 47,857

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 4 day of February, 2004.

Peter Rebuffoni

gnature

PTO/SB/08A(10-01)
Approved for use through 10/31/2002. OMB 951-0031
US Patent & Trademark Office: U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Substitute for form 1449A/PTO Complete if Known INFORMATION DISCLOSURE 09/945535 **Application Number** STATEMENT BY APPLICANT August 30, 2001 Filing Date (Use as many sheets as necessary) Ahn, Kie **First Named Inventor Group Art Unit** 2813 FEB 1 1 200% Blum, David **Examiner Name** Attorney Docket No: 1303.026US1

US PATENT DOCUMENTS								
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate		
<u>.</u>	US- 2001/0009695	07/26/2001	Saanila, Ville A., et al.	427	255.39	01/18/2001		
	US- 2002/0146916	10/10/2002	Irino, Kiyoshi, et al.	438	785	03/29/2002		
	US- 2003/0175411	09/18/2003	Kodas, T. T., et al.	427	58	10/04/2002		
	US-6,093,944	07/25/2003	VanDover, R B.	257	310	06/04/1998		
	US-6,451,695	09/17/2002	Sneh, O.	438	685	12/22/2000		
	US-6,602,338	08/05/2003	Chen, S., et al.	106	287.19	04/11/2001		

FOREIGN PATENT DOCUMENTS									
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²			

	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the itel (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
		CHAMBERS, J J., et al., "Physical and electrical characterization of ultrathin	
		yttrium silicate insulators on silicon", <u>Journal of Applied Physics, 90(2)</u> , (July 15, 2001), 918-33	
		KUKLI, KAUPO, et al., "Low-Temperature Deposition of Zirconium Oxide-Based Nanocrystalline Films by Alternate Supply of Zr[OC(CH3)3]4 and H2O", Chemical Vapor Deposition, 6(6), (2000), 297-302	
		NAKAJIMA, ANRI, "Soft breakdown free atomic-layer-deposited silicon- nitride/SiO/sub 2/ stack gate dielectrics", <u>International Electron Devices Meeting</u> . Technical Digest, (2001), 6.5.1-4	
		RAHTU, ANTTI, et al., "Atomic Layer Deposition of Zirconium Titanium Oxide from Titanium Isopropoxide and Zirconium Chloride", Chemistry of Materials, 13(5), (May 2001), 1528-1532	
		WOLF, S., et al., In: Silicon Processing of the VLSI Era, Vol. 1, Lattice Press, 374-380	

EXAMINER

Sheet 1 of 1

DATE CONSIDERED